Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(substrate adj tub) and precharge	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:30
L2	454	(substrate adj bias) and precharge	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:30
L3	46	2 and (floating adj gate)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:30
L4	11	3 and string$1	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:30
L5	10	4 and program$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:31
L6	1	5 and inhibit$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:33
L7	2	("5677873").PN.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:34
L8	0	(substrate adj tub) and (floating adj gate) and (program adj voltage) and inhibit and cahnnel and string and (precharge adj voltage).clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:35
L9	0	(substrate adj tub) and (floating adj gate) and (program adj voltage) and inhibit and channel and string and (precharge adj voltage).clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:35
L10	0	(substrate adj tub) same NAND same (floating adj gate) same (inhibit adj voltage) same channel same (precharge adj voltage).ab.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/07/24 16:36